

Job opportunity - FPGA design Engineer:

eVS designs computer vision software on embedded architectures. We are a high-tech company strongly oriented towards research and innovative solutions for machine vision, driving assistance, security and biomedical engineering.

Our mission is to provide software modules and FPGA IP blocks that add value to the embedded solutions of our customers and reduce their time-to-market. These modules are the result of a constant improvement to achieve the higher performance with the minor resource consumption. The aim is to produce the most cost-effective, customizable and easy-to-integrate solution.

This is where an FPGA Engineer at eVS comes in. As an FPGA Engineer here, your job doesn't stop at designing or developing technology. You're also expected to solve problems, spot new opportunities and drive them through to completion.

eVS is currently looking for an FPGA Engineer with a deep understanding of technology and computer architecture to strengthen the team. You should be able to design and develop FPGA projects, be adaptable to our existing structure, and also bring in your own ideas on how to improve designs. The focus of your job will be to design and develop FPGA based systems implementing vision and machine learning algorithms.

Desired skills and experience:

- Background in Electrical Engineering, Computer Engineering, Computer Science or a related technical field. Knowledge about signal and image processing is a plus.
- At least two years of FPGA related development experience. Understanding of FPGA development environments and FPGA design flow in particular for Xilinx devices.
- Proficiency in VHDL, C/C++. Ability to work in a Linux environment and basics of drivers development. Experience with MATLAB and scripting languages is a plus.
- Ability to design, implement, and document FPGA module level designs from module level specifications.
- Ability to work independently and collaboratively in team environments. Communication skills.

Mail eVS at info@evsys.it to apply for this position.